		STUDY MODULE D	ESCRIPTION FORM		
	f the module/subject oprocessor tech	nology	Code 1010322321010321118		
Field of	-	nology	Profile of study	Year /Semester	
Electrical Engineering			(general academic, practical) (brak)	1/2	
Elective path/specialty			Subject offered in:	Course (compulsory, elective)	
Cycle o		ystems in Mechatronics	Polish Form of study (full-time,part-time)	obligatory	
0,010 0					
Second-cycle studies			full-time		
No. of hours Lecture: 15 Classes: - Laboratory: 15			Draiaat/aaminara:	No. of credits 2	
	0100000	s: - Laboratory: 15 program (Basic, major, other)	Project/seminars: • (university-wide, from another field		
		(brak)	(br	ak)	
Educati	on areas and fields of sci		ECTS distribution (number and %)		
techr	nical sciences			2 100%	
	Technical scie	ences		2 100%	
Resp	onsible for subje	ect / lecturer:			
	nż. Grzegorz Trzmiel				
	ail: Grzegorz.Trzmiel@	put.poznan.pl			
	616652693 dział Elektryczny				
	Piotrowo 3A 60-965 Pc	oznań			
		s of knowledge, skills an	d social competencies:		
			s, physics, fundamentals of electric	cal engineering and	
1	Knowledge	electronics, including digital.	s, physics, fundamentals of electric	cal engineering and	
2	Skills		erpret knowledge transmitted in th Id related to the chosen field of stu		
3	Social competencies	The awareness of the need to eather the team.	xpand their competence, their willin	ngness to cooperate within	
Assu	mptions and obj	ectives of the course:			
		retical and practical problems ass the basis of their programming a	ociated with the construction elem	ents, components and	
	Study outco	mes and reference to the	educational results for a	field of study	
Knov	vledge:				
1. characterize the structure and principles of the basic elements and the processor - [K_W07+++, K_W10++]					
			ems - [K_W07+++, K_W18++, K_V	-	
Skills		er programming using object-oner	nted programming elements - [K_V	v07+++j	
1. appl			to determine the important parame	eters of of data transmission	
 obtain information from the literature and the Internet, work individually and independently solve problems in the theory of systems analysis and design and microprocessor devices - [K_U01++, K_U07+] 					
Socia	al competencies:				
1. able	to think and act in an	entrepreneurial manner in the are	a of analysis microprocessors - [K	_K01+, K_K02++]	

Assessment methods of study outcomes

Lecture:

- Assess the knowledge and skills shown on the completion of writing of microprocessor technology.

Laboratory:

- Test and rewarding knowledge necessary for the accomplishment of the problems in the area of ??laboratory tasks.
- Continuous assessment for each course rewarding the increase in the ability to use principles and methods have met.

- Assess the knowledge and skills related to the implementation of the tasks of exercises, evaluation of individual tasks in practice.

Get extra points for activity in the classroom, and in particular for:

- Proposing to discuss additional aspects of the subject,
- The effectiveness of applying knowledge when solving a given problem,
- Ability to work within a team practically performing the task detailed in the laboratory,
- Comments relating to the improvement of teaching materials,
- Aesthetic diligence reports and jobs in the framework of self-study

Course description

Applied methods of education: lectures: 15 h., laboratories: 15 h.

Lecture:

The idea of pipelining. Architecture microprocessors. Construction, types (classifications), features and basic functionality of a microcontroller. Microcontrollers closed (embedded). The microprocessor core. The oscillator and clock signal distribution systems. Methods for power reduction. Special modes microcontroller. RESET. Sources RESET. Systems supervising the correct operation of the microcontroller. Watchdog. Methods of cooperation with peripherals. Systems interrupts. Programming nested. Basic programming languages. Commissioning and testing programs. CAN interface: features, systems, types of frames (without detailed structures), model of communication, error detection mechanisms, concepts construction node, electromagnetic interference advantages. LIN interface. Profibus.

Updated 2017: Presenting innovative solutions in the field of microprocessor technology, applied in the latest solutions in various industries.

A multimedia presentation with figures, diagrams, photos, supplemented with practical examples on the board, slides and computer programs, facilitating the linking of theory to practice. Lecture supplemented with additional materials provided to students for self study.

Use students' knowledge of other subjects, initiate discussions, ask questions to increase student activity and autonomy.

Laboratories: Getting to know the architecture of an exemplary microcontroller and microcontroller programming in C in terms of handling internal and external devices. Basics of C51 language specification, implementation programs, use of selected internal systems, among others, timers and interrupt system, serial, AC transducer. Implementation of external devices, among others, LCD, LED, matrix keyboard. Implementation of the exemplary cooperation project microprocessor system with an external device.

Using tools to enable students to perform tasks at home (microcontroller simulator with peripherals, specialized software for programming microcontrollers). Classes at the university supplemented by materials for self-employment on free software packages.

Basic bibliography:

1. Jabłoński T., Pławsiuk K., Programowanie mikrokontrolerów PIC w języku C, BTC, Warszawa 2005.

2. Krzyżanowski R., Układy mikroprocesorowe, Mikom, Warszawa 2004.

3. Pietraszek S., Mikroprocesory jednoukładowe PIC, Wyd. Helion, Gliwice, 2002.

Additional bibliography:

1. Jabłoński T., Mikrokontrolery PIC16F8x w praktyce, Wyd. BTC, Warszawa, 2002.

2. Francuz T., Język C dla mikrokontrolerów, od podstaw do zaawansowanych aplikacji, Helion, Gliwice 2011,

3. Tatjewski P., Sterowanie zaawansowane obiektów przemysłowych. Struktury i algorytmy, Akademicka Oficyna Wydawnicza EXIT, Warszawa, 2002.

4. Piasecki A., Trzmiel G., Remote building control using the bluetooth technology, Monograph Computer Applications in Electrical Engineering, Poznan University of Technology 2016, vol. 14, pp. 457 ? 468.

6. Internet.

Result of average student's workload

Activity

Time (working hours)

^{5.} Diploma theses.

1. participation in class lecture	15	
2. participation in laboratory classes	15	
3. consultation on the lecture	2	
4. consultation on the laboratory	3	
5. preparation to pass	10	
6. pass	2	
7. preparation for laboratory exercises and pass the laboratory	12	
8. grade the laboratory	2	
Student's wo	rkload	
Source of workload	hours	ECTS
Total workload	61	2
Contact hours	39	1
Practical activities	32	1